




Date : 29/06/2017

NOTICE

All the students of SE /TE/BE are hereby informed that Bridge course is scheduled from 03/07/2017 to 07/07/ 2017 as per the time table displayed on notice board. Attendance is compulsory. Bridge course is scheduled in following class room

SR NO	CLASS	CLASS ROOM NO
1	SE	211,205
2	TE	212
3	BE	213,211


HOD

(Mrs. Nilima Zade)

H. O. D.
Electronics Dept.
Padmabhushan Vasantdada Patil Pratishtha
College of Engineering
Mumbai - 400 022

PADMABHUSHAN VASANTDADA PATIL PRATISHTHAN'S COLLEGE OF ENGINEERING, SION (E) - MUMBAI-22
BRIDGE TIME TABLE

SH-17-BE

TITLE : ACADEMIC SCHEDULE (SECOND HALF - 2017)	
DEPT : ELECTRONICS ENGINEERING	
W.E.F. : 03/07/2017 to 07/07/17	CLASSROOM NO-213 ,211
CLASS - BE (SEM VII)	

TIME(HRS) DAYS	9:00 TO 10:00	10:00 TO 11:00	11:00 TO 11:10	11:10 TO 12:10	12:10 TO 13:10	13:10 TO 13:40	13:40 TO 14:50	14:50 TO 16:00	16:00 TO 17:00
MONDAY	ICT (DP)	AI-(NZ)-211 DIP-(SK)-213	B	PE-II (JP)	PE-II (JP)	L	CCN (KS)	ESD (KW)	ESD (KW)
TUESDAY	ESD (KW)	ESD (KW)	R	PE-II (JP)	AI-(NZ)-211 DIP-(SK)-213	U	CCN (KS)	ICT (DP)	PE-II (JP)
WEDNESDAY	PE-II (JP)	ESD (KW)	E	ICT (DP)	AI-(NZ)-211 DIP-(SK)-213	N	CCN (KS)	CCN (KS)	ICT (DP)
THURSDAY	AI-(NZ)-211 DIP-(SK)-213	AI-(NZ)-211 DIP-(SK)-213	A	ICT (DP)	ESD (KW)	C	CCN (KS)	PE-II (JP)	ICT (DP)
FRIDAY	CCN (KS)	ESD (KW)	K	AI-(NZ)-211 DIP-(SK)-213	PE-II (JP)	H	ICT (DP)	CCN (KS)	AI-(NZ)-211 DIP-(SK)-213

SUB	CLASS TEACHER	THEORY	TUTORIAL	TOTAL
AI-Artificial Intelligence	/NZ - Mrs. Nilima Zade	07	-	07
DIP-Digital Image Processing	/SK-Dr. Swati Khandare	07	-	07
PE-II-Power Electronics -II	/JP- Mrs. Jayashree Pawar	07	-	07
ESD-Embedded system Design	/KW-Mrs. Kavita Wagh	07	-	07
CCN-Computer Communication Network	/KS-Mrs. Khushboo Singh	07	-	07
ICT-IC Technology	/DP-Mrs. Dharti Patel	07	-	07
			TOTAL	42

Soni
 Mrs. Sonali Rahimode /Mrs. Anjali Pansare
 (Time Table Committee Member)
 P.V.PF. DM-ELEX -FF-04/A Revision 01 07/07/2017

Git
 Mrs. Gitamjali Korgotkar
 (Time Table Committee Convener)

Shah
 Mrs. Nilima Zade
 (I/C HOD ELEX)

Aleer
 Dr. Alam N. Shaikh
 (PRINCIPAL)

PADMABHUSHAN VASANTDASA PATIL PRATISHTHAN'S COLLEGE OF ENGINEERING, SION (E) -MUMBAI-22
BRIDGE TIME TABLE

SH-17-TE

TITLE : ACADEMIC SCHEDULE (SECOND HALF - 2017)
DEPT : ELECTRONICS ENGINEERING
W.E.F. : 03/07/2017 to 07/07/2017
CLASS - TE (SEM V)
CLASSROOM NO-212

TIME(HRS) DAYS	9:00 TO 10:00	10:00 TO 11:00	11:00 TO 11:10	11:10 TO 12:10	12:10 TO 13:10	13:10 TO 13:40	13:40 TO 14:50	14:50 TO 16:00	16:00 TO 17:00
MONDAY	DLIC (PM)	DC (PG)	B	BCE (MG)	MCA (PK)	L	SS (SK)	EME (RS)	MCA (PK)
TUESDAY	MCA (PK)	EME (RS)	R	DC (PG)	DLIC (PM)	U	BCE (MG)	SS (SK)	SS (SK)
WEDNESDAY	SS (SK)	EME (RS)	E	DC (PG)	DLIC (PM)	N	BCE (MG)	MCA (PK)	EME (RS)
THURSDAY	MCA (PK)	DC (PG)	A	EME (RS)	SS (SK)	C	DLIC (PM)	DLIC (PM)	BCE (MG)
FRIDAY	DLIC (PM)	DC (PG)	K	EME (RS)	MCA (PK)	H	SS (SK)	BCE (MG)	BCE (MG)

SUB	CLASS TEACHER	THEORY	TOTAL
DLIC - Design With Linear Integrated Circuit	PM - Mr. P.M.Manegopale	06	06
MCA- Micro-Controller And Application	PK- Pramod K B	06	06
EME - Electromagnetic Engineering	/RS-Dr. Rukhsana Sayed	06	06
DC - Digital Communication	/PG- Mrs. Priya Gupta	05	05
SS - Signals & Systems	/SK-Dr. Swati khandare	06	06
BCE - Business Communication And Ethics	MG - Manjunath Gawali	06	06
TOTAL			35

Singh
 Mrs. Sonali Pakhmode /Mrs. Anjali Pansare
 (Time Table Committee Member)
PVPP-DM-ELEX-FF-04/A Revision 01 07/07/2017

CS
 Mrs. Gitanjali Korgaonkar
 (Time Table Committee Convener)

Asad
 Mrs. Nilima Zade
 (I/C HOD ELEX)

Allex
 Dr. Alam N. Shaikh
 (PRINCIPAL)

CE 03 ELEX 04

PADMABHUSHAN VASANTDADA PATIL PRATISHTHAN'S COLLEGE OF ENGINEERING, SION (E) -MUMBAI-22
BRIDGE TIME TABLE

TITLE : ACADEMIC SCHEDULE (SECOND HALF - 2017-18)	CLASSROOM NO-211,205
CLASS - SE (SEM III)	
DEPT : ELECTRONICS ENGINEERING	
W.E.F. : 03/07 / 2017 to 07/07/2017	

TIME(HRS) DAYS	9:00 TO 10:00	10:00 TO 11:00	11:00 TO 11:10	11:10 TO 12:10	12:10 TO 13:10	13:10 TO 13:40	13:40 TO 14:50	14:50 TO 16:00	16:00 TO 17:00
MONDAY	MATHS -III (AYS)	OOPM (205)(SB)	B	DCD (/RS)	EDC-I (PM)	L	ENAS (/SP)	EIM (/LG)	OOPM (205)(SB)
TUESDAY	EDC-I (PM)	MATHS -III (AYS)	R	EIM (/LG)	OOPM (205)(SB)	U	DCD (/RS)	ENAS (/SP)	MATHS -III (AYS)
WEDNESDAY	MATHS - III (AYS)	EIM (/LG)	E	OOPM (SB)	ENAS (205)(/SP)	N	DCD (/RS)	EDC-I (PM)	EIM (/LG)
THURSDAY	ENAS (205)(/SP)	EDC-I (205)(PM)	A	EIM (/LG)	MATHS - III (AYS)	C	DCD (/RS)	OOPM (SB)	EDC-I (PM)
FRIDAY	OOPM (SB)	EDC-I (PM)	K	EIM (205)(/LG)	ENAS (/SP)	H	MATHS - III (AYS)	DCD (/RS)	ENAS (/SP)

SUB	CLASS TEACHER	THEORY	TUTORIAL	TOTAL
AM-III -Applied Mathematics III	AYS-Mr. A Y Shete	06		06
EDC-1 - Electronic Device and Circuits I	PM -Mr. P.M. Manegopale	06		06
EANS-Electrical Network Analysis and Synthesis	/SP - Mrs Sonali Pakhmode	06		06
OOPM-Object Oriented Programming Methodology	SB-Srikant Bagewadi	06		06
DCD-Digital Circuit Design	/RS-Dr. Rukhasana Sayed	05		05
EIM-Electronics Instruments and Measurement	/LG- Leena Govekar	06		06
			TOTAL	35

Soni
Mrs. Sonali Pakhmode /Mrs. Anjali Pansare
(Time Table Committee Member)

PVPP- DM- E/EX- FF-04/A Revision 01 07/07/2017

G
Mrs. Gitanjali Korgaonkar
(Time Table Committee Convener)

A Zade
Mrs. Nilima Zade
(I/C HOD ELEX)

Alam
Dr-Alam N. Shaikh
(PRINCIPAL)

SH-17-SE